

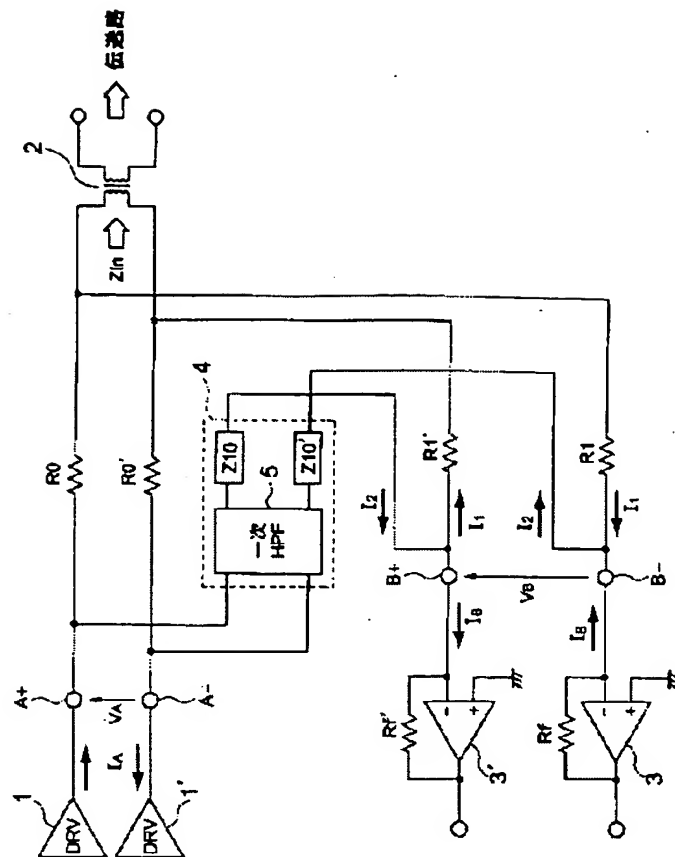
PUBLICATION NUMBER : 2002335193
 PUBLICATION DATE : 22-11-02
 APPLICATION DATE : 09-05-01
 APPLICATION NUMBER : 2001138095

APPLICANT : NEC MIYAGI LTD;

INVENTOR : OGATA TAKENORI;

INT.CL. : H04B 3/03 H04Q 3/42

TITLE : TWO LINE/FOUR LINE CONVERSION
 CIRCUIT



ABSTRACT : PROBLEM TO BE SOLVED: To provide a two line/four line conversion circuit in which echo components can be suppressed over a wide band.

SOLUTION: In order to suppress echo signals in a two line/four line converting circuit, a balance network 4, comprising a cascade connection of a primary high-pass filter 5 for suppressing echo on the low region side and an impedance circuit Z10 of a resistive element, a capacitive element and an inductance element for suppressing echo on the intermediate and high regions, is provided, and the echo signal is suppressed over a wide band at the adding nodes B+ and B- of receiver amplifiers 3 and 3' constituting a current addition circuit.

COPYRIGHT: (C)2003,JPO